

DG754880 D1E3D1

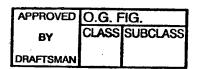


Fig. 2A

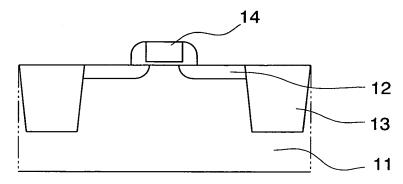


Fig. 2B

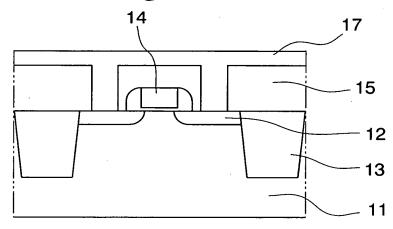


Fig. 2C

S URFACE LEVEL OF

SURFACE LEVEL OF FIRST INTERLAYER INSULATING FILM AFTER POLYSILICON IS ETCHED BACK

14

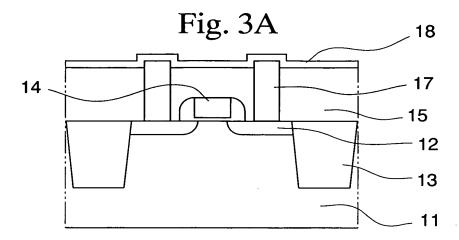
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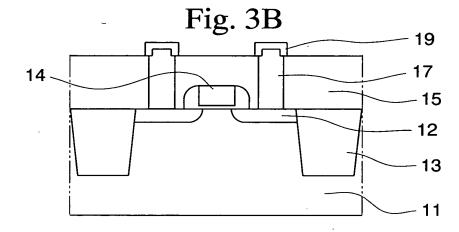
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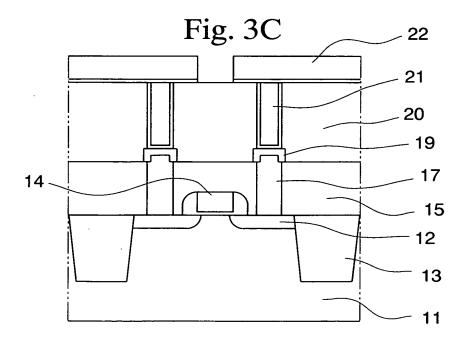
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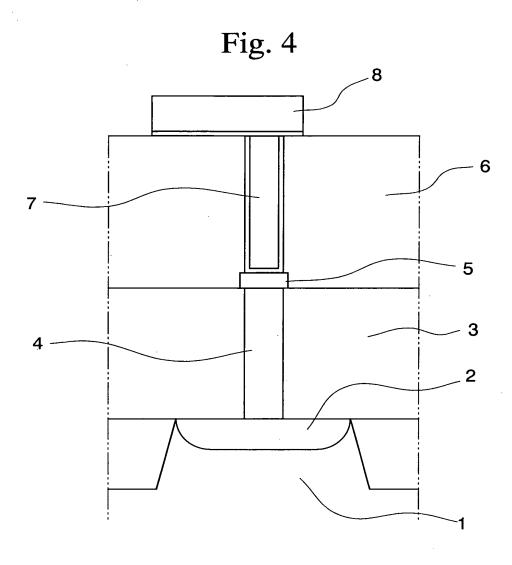
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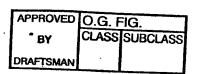
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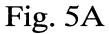












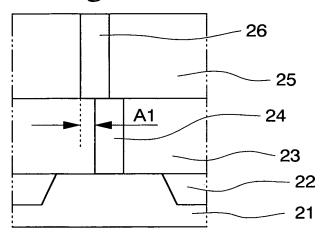


Fig. 5B

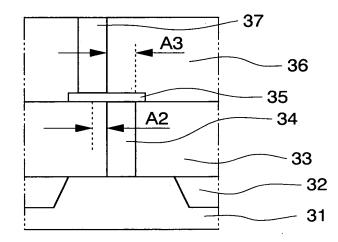


Fig. 5C

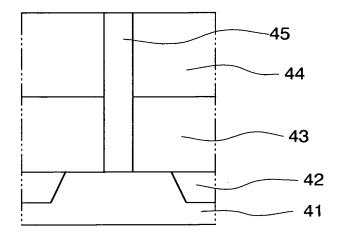
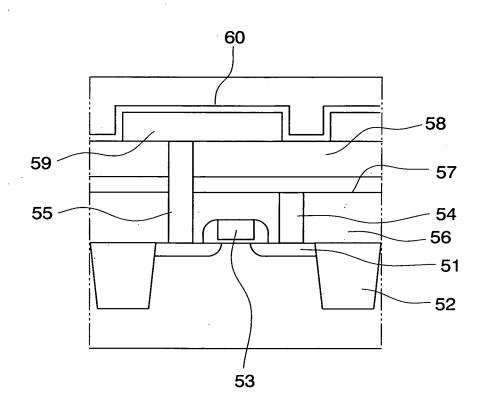




Fig. 6



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